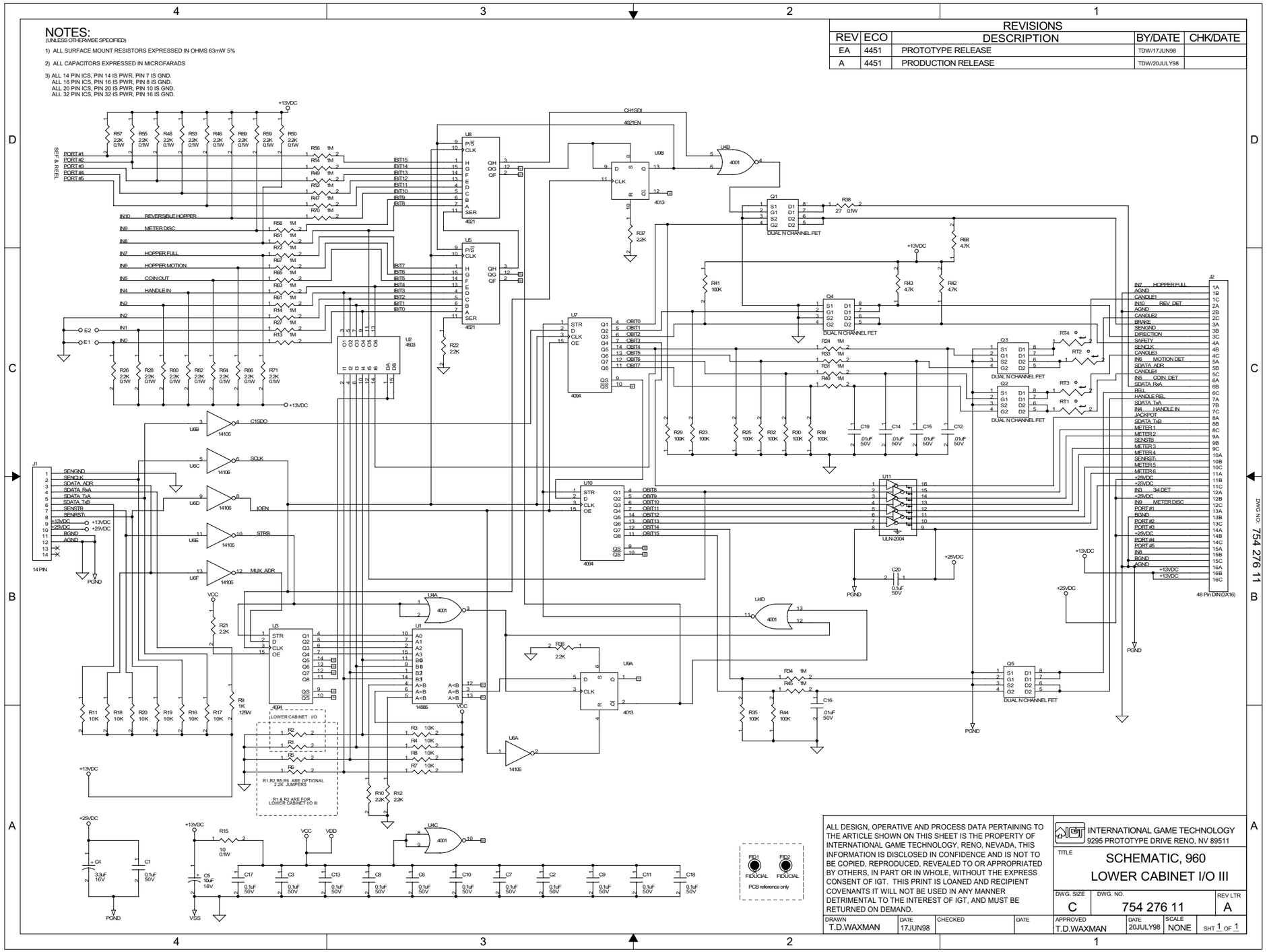
NOTES:
(UNLESS OTHERWISE SPECIFIED)
1) ALL SURFACE MOUNT RESISTORS EXPRESSED IN OHMS 63mW 5%
2) ALL CAPACITORS EXPRESSED IN MICROFARADS
3) ALL 14 PIN ICS, PIN 14 IS PWR, PIN 7 IS GND.
ALL 16 PIN ICS, PIN 16 IS PWR, PIN 8 IS GND.
ALL 20 PIN ICS, PIN 20 IS PWR, PIN 10 IS GND.
ALL 32 PIN ICS, PIN 32 IS PWR, PIN 16 IS GND.
REVISIONS
REV ECO DESCRIPTION BY/DATE CHK/DATE
EA 4451 PROTOTYPE RELEASE TDW/17JUN98
A 4451 PRODUCTION RELEASE TDW/20JULY98
SEF & REEL
PORT#1
PORT#2
PORT#3
PORT#4
PORT#5
IN10 REVERSIBLE HOPPER
IN9 METER DISC
IN8
IN7 HOPPER FULL
IN6 HOPPER MOTION
IN5 COIN OUT
IN4 HANDLE IN
IN3
IN2
IN1
IN0
E2
E1
+13VDC
CH1SDI
4021EN
U8
U5
4021
U8B
4001
U4B
U6B
U6D
U6E
U6F
14106
C1SDO
SCLK
IOEN
STRB
MUX ADR
U2
4003
U7
4094
U10
U11
ULN-2004
Q1
Q2
Q3
Q4
Q5
DUAL N CHANNEL FET
RT1
RT2
RT3
RT4
J1
14 PIN
SENGND
SENCLK
SDATA ADR
SDATA RxA
SDATA TxA
SDATA TxB
SENSTB
SENRST\
+25VDC
BGND
AGND
PGND
J2
48 Pin DIN (3X16)
INZ HOPPER FULL
CANDLE1
IN10 REV DET
CANDLE2
BRAKE
SENGND
DIRECTION
SAFETY
SENCLK
CANDLE3
IN6 MOTION DET
SDATA ADR
CANDLE4
IN5 COIN DET
SDATA RxA
REL1
HANDLE REL
SDATA TxA
IN4 HANDLE IN
JACKPOT
SDATA TxB
METER 1
METER 2
SENSTB
METER 3
METER 4
SENRST\
METER 5
METER 6
IN9 METER DISC
PORT#1
PORT#2
PORT#3
PORT#4
PORT#5
IN3 3/4 DET
U1
U3
4094
U4A
U4C
U4D
U9A
4013
14585
U6A
LOWER CABINET I/O
R1,R2,R5,R6 ARE OPTIONAL 2.2K JUMPERS
R1 & R2 ARE FOR LOWER CABINET I/O III
FID1 FIDUCIAL
FID2 FIDUCIAL
PCB reference only
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INTERNATIONAL GAME TECHNOLOGY
9295 PROTOTYPE DRIVE RENO, NV 89511
TITLE
SCHEMATIC, 960
LOWER CABINET I/O III
DWG. SIZE C
DWG. NO. 754 276 11
REV LTR A
DRAWN T.D.WAXMAN
DATE 17JUN98
CHECKED
DATE
APPROVED T.D.WAXMAN
DATE 20JULY98
SCALE NONE
SHT 1 OF 1
DWG. NO. 754 276 11